



Minneapolis, MN  
July 12-15, 2006

**Welcome to ICPADS 2006**  
**Minneapolis**

***Timothy M. Pinkston, Program Chair***  
***National Science Foundation***  
***University of Southern California***





## *Program Overview*

Minneapolis, MN  
July 12-15, 2006

- Four session keynote speakers
- A banquet keynote speaker
- A panel session
- Twenty-five technical sessions
  - Seven parallel sessions with up to four tracks
- Three workshops





## Keynote Addresses

Minneapolis, MN  
July 12-15, 2006

- **Wednesday 9:00 - 10:00 am:** “Blue Gene/L, a Massively Parallel Supercomputer: An Applications Point-of-View,” *Carlos P. Sosa*, IBM and University of Minnesota Supercomputing Institute
- **Thursday, 9:00 - 10:00 am:** “Mobile Sensor Networks & the Princeton ZebraNet Project: Experiences and Challenges,” *Margaret Martonosi*, Princeton University
- **Thursday, 4:45 - 5:45 pm:** “Emerging Networking Technologies and Protocols for Next Generation Clusters,” *Dhabaleswar (D.K.) Panda*, The Ohio State University
- **Friday, 9:00 - 10:00 am:** “An Overview of Middleware Architectures for Scheduling Grid Applications,” *Frédéric Desprez*, INRIA-ENS Lyon, France





## *Panel and Banquet*

Minneapolis, MN  
July 12-15, 2006

- **Panel Session: Thursday, 4:30 - 6:00 pm**

“How will we develop and program emerging robust, low-power, adaptive multicore computing systems?”

Moderator: *David J. Lilja*, University of Minnesota

Panelists: *Dennis Abts*, Cray, Inc.

*Mark Hill*, University of Wisconsin, Madison

*Margaret Martonosi*, Princeton University

*Jose Moreira*, IBM T. J. Watson Research

*D.K. Panda*, The Ohio State University

- **Banquet: Thursday, 6:00 – 8:00 pm**

Banquet Speaker: *Craig Stunkel*, IBM T.J. Watson Research Center,  
“HPC: Been there, done that?”

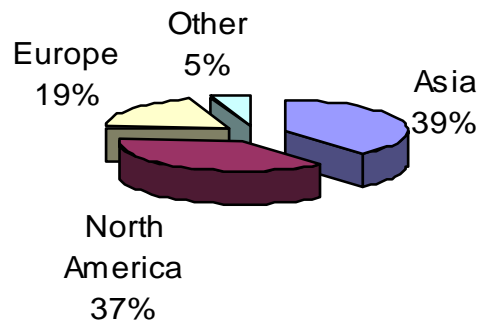




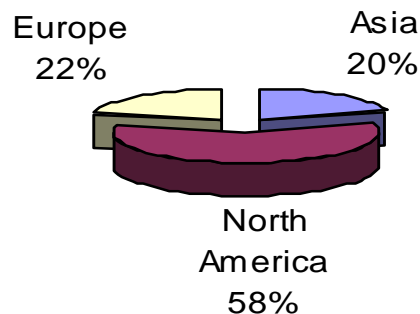
# Contributed Papers

Minneapolis, MN  
July 12-15, 2006

## Submission Rate

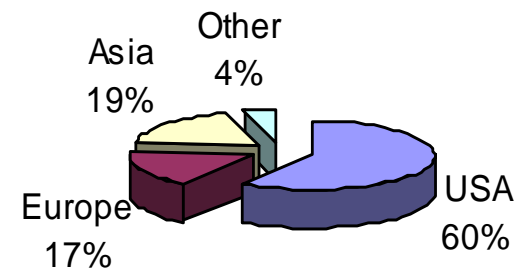


## Acceptance Rate



- 183 total submissions from 26 countries
- 69 total acceptances (38%) from 12 countries
- All papers received at least 3 reviews, on-line deliberation
- PC members: 143 come from 25 different countries

## Program Committee





Minneapolis, MN  
July 12-15, 2006

## PC Membership:

85 from US, 27 from Asia

25 from Europe,

3 from Canada, 2 from  
Australia, and

1 from Brazil

Members:

Australia, Belgium, Brazil, Canada, China,  
France, Germany, Greece, Hong Kong,  
India, Ireland, Japan, Korea, the  
Netherlands, Norway, Poland, Portugal,  
Romania, Singapore, Spain, Sweden  
Taiwan, Thailand, UK and USA





## Technical Sessions

Minneapolis, MN  
July 12-15, 2006

### Wednesday, July 12

- **Session I-A:** Heterogeneous Scheduling
- **Session I-B:** Stabilization Algorithms
- **Session I-C:** Availability and Security
- **Session I-D:** MPI & Shared Memory
  
- **Session II-A:** Scheduling and Resource Management
- **Session II-B:** Parallel and Distributed Algorithms I
- **Session II-C:** Web Performance
- **Session II-D:** Data Dissemination in Sensor Networks
  
- **Session III-A:** QoS in Interconnection Networks
- **Session III-B:** Streaming
- **Session III-C:** Sensor Network Deployment

### Thursday, July 13

- **Session IV-A:** Power-aware Protocols
- **Session IV-B:** Parallel & Distributed Algorithms II
- **Session IV-C:** Grids
- **Session IV-D:** Multiprocessor Scheduling
  
- **Session V-A:** Multicore Scheduling
- **Session V-B:** P2P Networks I
- **Session V-C:** Switch Design
  
- **Session VI-A:** Load Balancing
- **Session VI-B:** Parallel Programming Tools
- **Session VI-C:** (short papers) Communication Networks

### Friday, July 14

- **Session VII-A:** Fault Tolerance
- **Session VII-B:** P2P Networks II
- **Session VII-C:** Resource Monitoring
- **Session VII-D:** Ad Hoc and Wireless Networks





## **Best Paper Awardees**

Minneapolis, MN  
July 12-15, 2006

- **Best Paper Award: Session I-D**

“Scalable Hybrid Designs for Linear Algebra on Reconfigurable Computing Systems,” Ling Zhuo and Viktor K. Prasanna, University of Southern California

- **Best Paper Award: Session V-A**

“Multi-Level On-Chip Memory Hierarchy Design for Embedded Chip Multiprocessors,” Ozcan Ozturk, Mahmut Kandemir, Mary Jane Irwin, Suleyman Tosun, Pennsylvania State University





## Acknowledgements

Minneapolis, MN  
July 12-15, 2006

- Thanks to the ICPADS'06 *Program Vice-Chairs*:
  - **Communication and Interconnection Networks**  
*Angelos Bilas*, FORTH and University of Crete, Greece
  - **Grid, P2P, Web-based and Cluster Computing**:  
*Cho-Li Wang*, University of Hong Kong, China, and  
*Lionel Ni*, Hong Kong University of Science and Technology, China
  - **Parallel Applications and Algorithms**  
*Yves Robert*, CNRS- ENS Lyon, INRIA, France
  - **Power-Aware and Reliable Computing**  
*Li Shiuan Peh*, Princeton University, USA
  - **Software Systems Resource Management**  
*Anand Sivasubramaniam*, Penn State University, USA
  - **System Architecture**  
*David Kaeli*, Northeastern University, USA
  - **Wireless/Mobile/Sensor Networks and Computing**  
*Ahmed Helmy*, University of Southern California, USA
- Thanks to Publications Chair: *Hironori Kasahara*, Waseda University, Japan
- Thanks to Publicity Chair: *Nian-Feng Tzeng*, University of Louisiana, Lafayette, USA
- Thanks to many others who contributed to this conference's success
- **Special Thanks to General Chair: *Pen-Chung Yew*, University of Minnesota, USA**

Country	#Papers Submitted	#Accepted	Acceptance rate	Submission rate
Taiwan	23	6	8.7%	12.6%
India	13	3	4.3%	7.1%
China	25	3	4.3	36.2
Singapore	1	0	0%	0.5%
UK	2	1	1.5%	1.1%
France	11	4	5.8%	6%
Egypt	1	0	0%	0.5%
Iran	5	0	0%	2.7%
Greece	5	3	4.3%	2.7%
Italy	1	0	0%	0.5%
Japan	7	2	2.9%	3.8%
Algeria	1	0	0%	0.5%
Pakistan	1	0	0%	0.5%
USA	60	37	53.6%	32.8%
Israel	1	0	0%	.5%
Finland	1	0	0%	0.5%
Ireland	1	0	0%	0.5%
Germany	4	1	1.5%	2.2%
Canada	7	3	4.3%	3.8%
Spain	6	5	7.2	3.3
Poland	1	0	0	.5
Korea	2	0	0	1.1
Austria	1	1	1.5	.5
Russia	1	0	0	.5
Brazil	1	0	0	.5
Denmark	1	0	0%	.5%
Total	47	117	100%	100%